

Fig. 1A

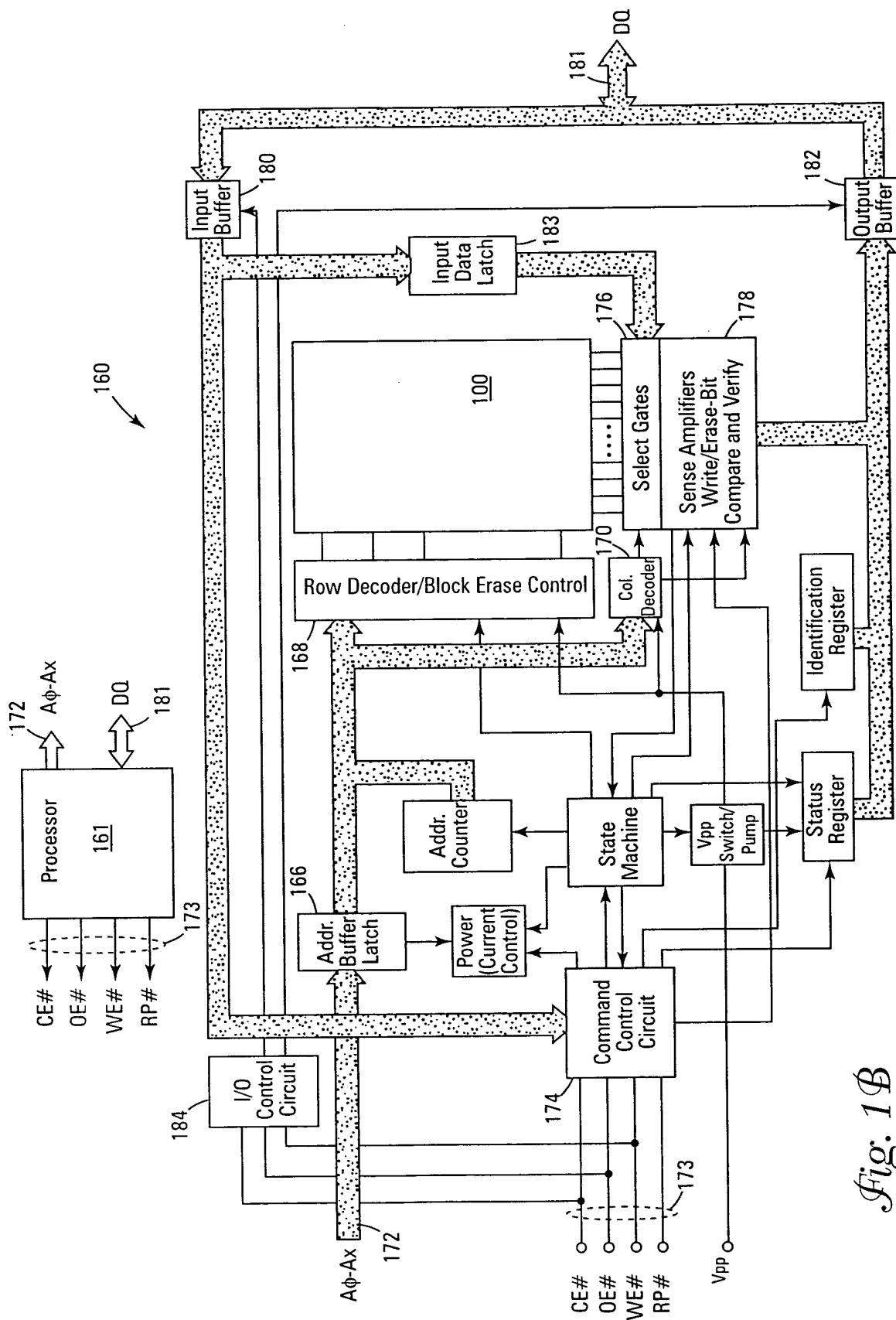


Fig. 1B

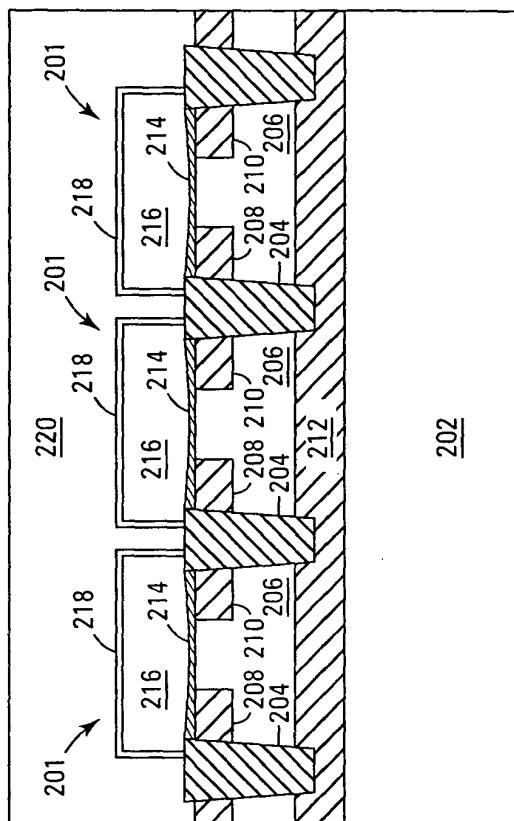


Fig. 2B

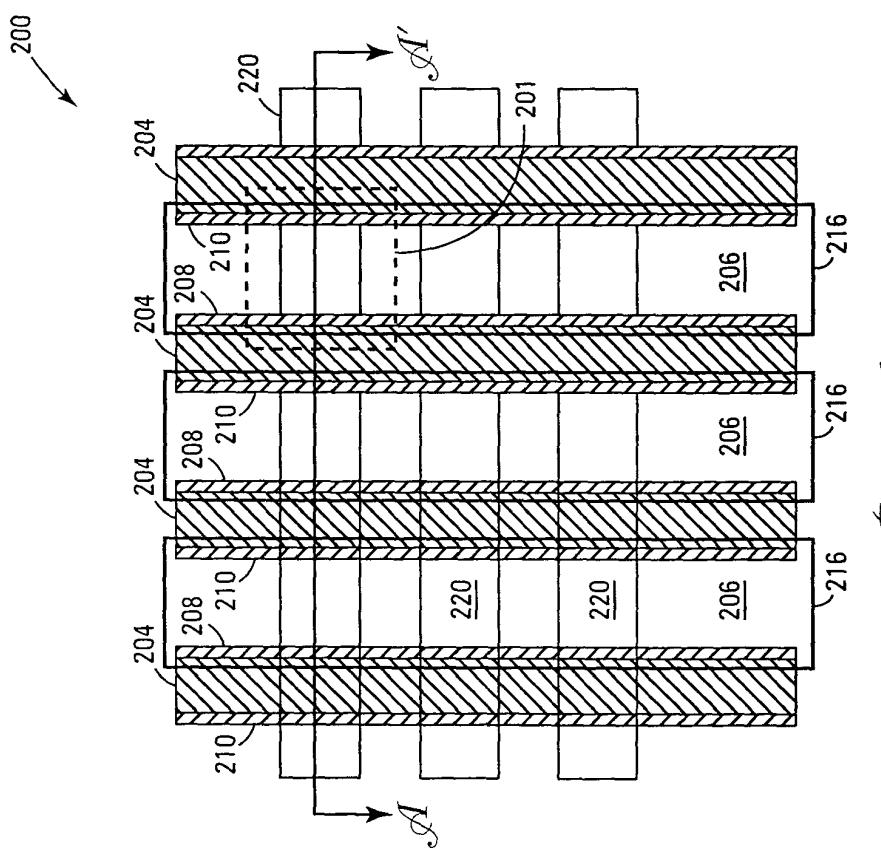


Fig. 2A

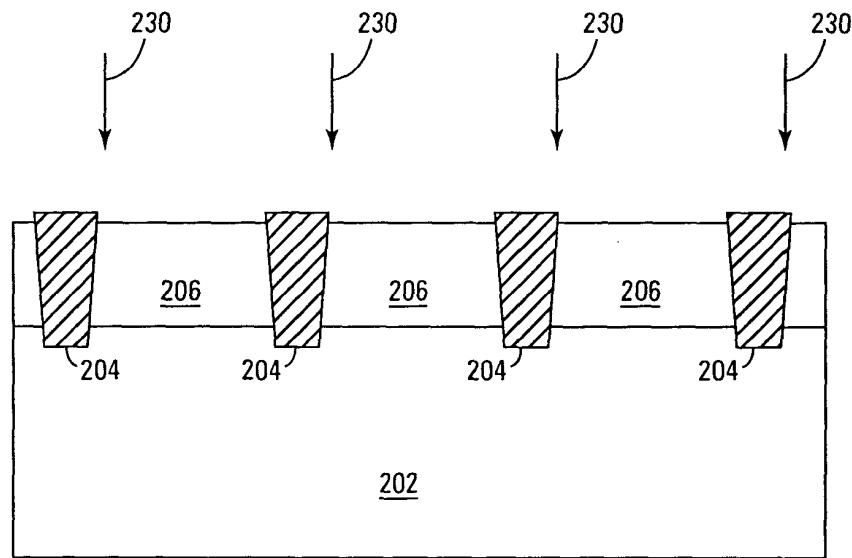


Fig. 3A

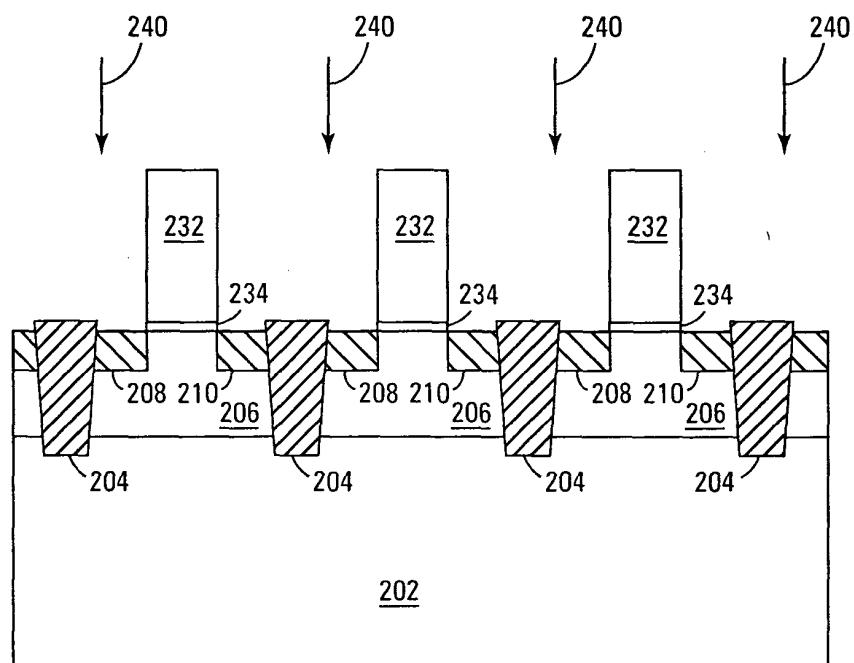


Fig. 3B

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CONTACTLESS UNIFORM-TUNNELING SEPARATE P-WELL (CUSP) NON-VOLATILE
MEMORY ARRAY ARCHITECTURE, FABRICATION AND OPERATION
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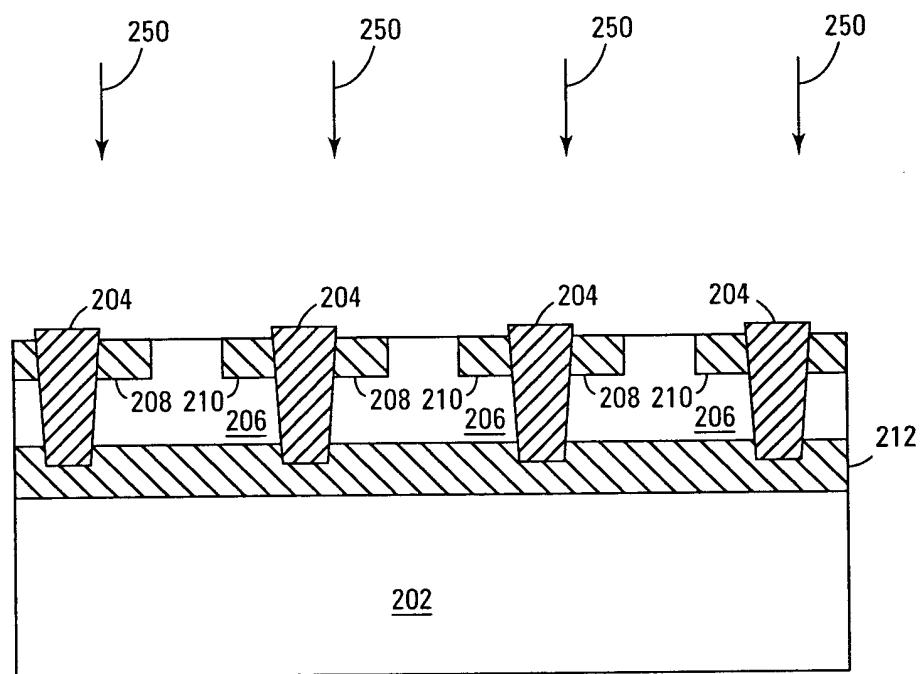


Fig. 3C

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MEMORY ARRAY ARCHITECTURE, FABRICATION AND OPERATION
Atty. Docket 400.181US03

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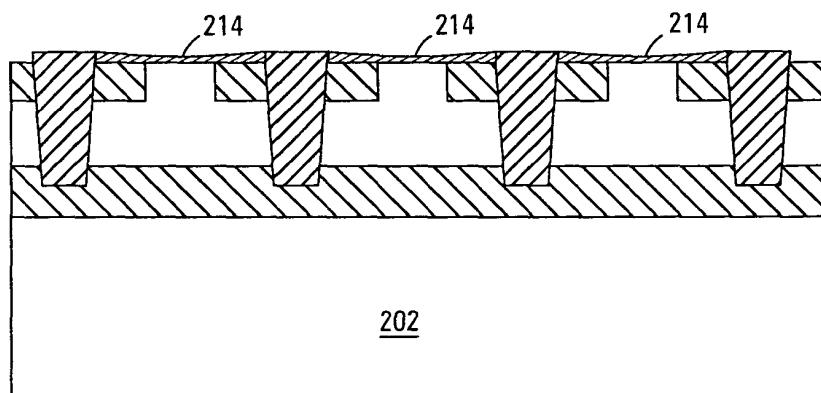


Fig. 3D

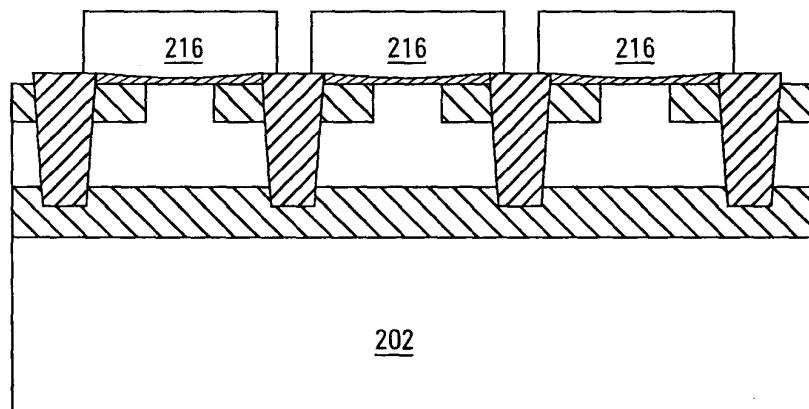


Fig. 3E

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MEMORY ARRAY ARCHITECTURE, FABRICATION AND OPERATION
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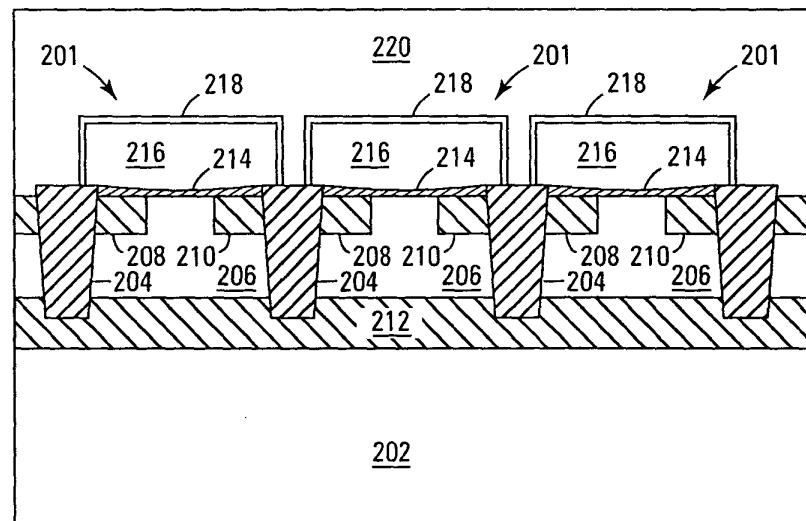


Fig. 3F